

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 03-29-2004
Application Type:: Regular
Subject Matter:: Utility
Title:: Method, System, and Apparatus for
Incremental Design in Programmable
Logic Devices Using Floorplanning
Attorney Docket Number:: X-1490 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 05
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: India
Status:: Full Capacity
Given Name:: Rajat
Family Name:: Aggarwal
Street:: 755 Lois Avenue
City:: Sunnyvale
State or Province:: California
Postal or Zip Code:: 94087

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Germany
Status:: Full Capacity
Given Name:: Guenter
Family Name:: Stenz
Street:: 1845 S. Bascom Avenue #27A
City:: Campbell
State or Province:: California

Postal or Zip Code:: 95008

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: India

Status:: Full Capacity

Given Name:: Srinivasan

Family Name:: Dasasathyan

Street:: 257 Arriba Drive #6

City:: Sunnyvale

State or Province:: California

Postal or Zip Code:: 94086

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.

Street:: 2100 Logic Drive

City:: San Jose

State or Province:: California

Postal or Zip Code:: 95124